



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/699,221 Confirmation No.: 2799  
First Named Inventor: Constantin Bulucea Filing Date: 31 October 2003  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127-1D US  
Title: Design and Operation of Gate-enhanced Junction Varactor With  
Gradual Capacitance Variation  
Assignee: National Semiconductor Corporation

Mountain View, California  
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**MAIL STOP ISSUE FEE  
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**AMENDMENT TO TEXT UNDER 37 CFR 1.312**

Sir:

Under the provisions of 37 CFR 1.312, please amend the above patent application as described below.

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